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ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

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|---|--|-----------------|------------|-------------|------|-------|----------|
| Title of Invention | JITTER-RESISTIVE DELAY LOCK LOOP CIRCUIT FOR LOCKING DELAYED CLOCK AND METHOD THEREOF | | | | | | |
| Application Number : 18/711,313 | | | | | | | |
| Confirmation Number: | | | | | | | |
| First Named Applicant: | | Jui-Hsing Tseng | | | | | |
| Attorney Docket Number: | | MTKP0186USA | | | | | |
| Art Unit: | | 2816 | | | | | |
| Examiner: | | Linh Nguyen | | | | | |
| Search string: | | (6069507).pn | | | | | |
| US Patent Documents | | | | | | | |
| Note: Applicant is not required to submit a paper copy of cited US Patent Documents | | | | | | | |
| init | Cite.No. | Patent No. | Date | Patentee | Kind | Class | Subclass |
| 1 | 1 | 6069507 | 2000-05-30 | Shen et al. | | 227 | 156 |
| Signature | | | | | | | |
| Examiner Name | | | | Date | | | |
| Linh Nguyen | | | | 02/16/2006 | | | |